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**REMARKS****RECEIVED  
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Claims 1-11 are pending in the application.

Applicant acknowledges with appreciation the Examiner's finding that claims 6 and 9 contain allowable subject matter. Applicant submits that claim 1, from which claims 6 and 9 depend, is patentable over the reference cited against it as demonstrated below. Accordingly, Applicant requests that the Examiner allow claims 6 and 9.

Claims 1-5, 7-8, and 11 stand rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,775,340 to Arslan et al.; and claim 10 stands rejected under 35 U.S.C. § 103(a) as being unpatentable over Arslan et al. in view of U.S. Patent No. 6,345,078 to Basso et al. Applicant respectfully traverses the rejections.

The Examiner conducted a telephone interview on May 15, 2007 with Applicant's undersigned representative, Mr. Dexter Chang (Reg. No. 44,071). Applicant and Mr. Chang thank the Examiner for her time and consideration. During the interview, Mr. Chang pointed out to the Examiner, and the Examiner agreed, that the cited portions of Arslan et al. include description of "[s]ynchronization ... [being] done prior to the demodulation process." Col. 3, lines 49-51 of Arslan et al. And, therefore, such portions of Arslan et al. do not disclose the claimed feature of "synchronization detection [being] made by using the demodulated synchronization signal." Mr. Chang also pointed out that the cited portions of Arslan et al. only include description of estimating channel clusters using extended pilot symbols, and the estimation output is down-sampled. And such portions of Arslan et al., therefore, also fail to disclose the claimed feature of "making channel estimation by using the pilot signal from which a predefined part of the pilot signal is removed."

In other words, Arslan et al., as cited and relied upon by the Examiner, fail to disclose,

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“[a] synchronization detecting apparatus making synchronization detection by using a pilot signal that comprises a plurality of bits and at least one bit of said plurality of bits is used as a synchronization signal, comprising:

a channel estimating unit making channel estimation by using the pilot signal from which a predefined part of the pilot signal is removed; and

a synchronization signal demodulating unit demodulating said at least one bit of the synchronization signal by using a result of the channel estimation, wherein

said predefined part is defined for each target bit of the synchronization signal to be demodulated and is defined so as to include the target bit; and

synchronization detection is made by using the demodulated synchronization signal,” as recited in claim 1.

(Emphasis added)

Accordingly, Applicants respectfully submit that claim 1, together with claims 2-5 and 7-8 dependent therefrom, is patentable over Arslan et al. for at least the foregoing reasons. Claim 11 incorporates features that correspond to those of claim 1 cited above, and is, therefore, patentable over Arslan et al. for at least the same reasons. The Examiner relied upon Basso et al. as a combining reference to specifically address the additional features recited in claim 10. As such, the addition of this reference would still have failed to cure the above-described deficiencies of Arslan et al., even assuming, arguendo, that such addition would have been obvious to one skilled in the art at the time the claimed invention was made. Accordingly, Applicant respectfully submits that claim 10 is patentable over the cited references for at least the foregoing reasons.

In view of the remarks set forth above, this application is in condition for allowance which action is respectfully requested. However, if for any reason the Examiner should consider this application not to be in condition for allowance, the Examiner is respectfully requested to telephone the undersigned attorney at the number listed below prior to issuing a further Action.

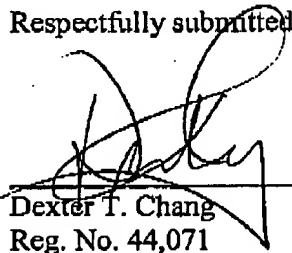
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Any fee due with this paper may be charged to Deposit Account No. 50-1290.

Respectfully submitted,



Dexter T. Chang  
Reg. No. 44,071

CUSTOMER NUMBER 026304

Telephone: (212) 940-6384

Fax: (212) 940-8986 or 8987

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